



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/829,380 Confirmation No. 7082  
Applicant : A. HAYASHI et al  
Filed : April 22, 2004  
Title : A SEMICONDUCTOR INTEGRATED CIRCUIT  
CONTROLLING OUTPUT IMPEDANCE AND SLEW RATE  
TC/AU : 2816  
Examiner : T. Lam  
Docket No. : H-1139  
Customer No.: 24956

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PETITION FOR EXTENSION OF TIME**

Sir:


It is respectfully requested that a three-month Extension of Time, to and including February 25, 2006, be granted in which to respond to the Office Action dated August 25, 2005 in the above-identified application.

Our Credit Card Payment Form in the amount of **\$1020.00** is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

By   
Shrinath Malur  
Reg. No. 34,663  
(703) 684-1120

February 27, 2006

02/28/2006 HALI11 00000092 10829380

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